

Notice of References Cited	Application/Control No. 10/661,299	Applicant(s)/Patent Under Reexamination CHEN ET AL.	
	Examiner Brook Kebede	Art Unit 2823	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,403,783	04-1995	Nakanishi et al.	438/106
	B	US-5,532,906	07-1996	Hanari et al.	361/749
	C	US-5,948,689	09-1999	Hamburgen et al.	436/122
	D	US-6,607,928	08-2003	Eiles et al.	438/16
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf, Silicon Processing for the VLSI Era Volume 2-Process Integration, pp 51-54, 1990.
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.